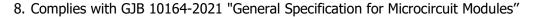


Description

D28CxxM20BP DC/DC has converter input voltage range 16V \sim 40V, output power of 20W,with operating temperature range of -55°C \sim +105°C. It adopts PCB surface mount technology and is encapsulated with metal case with potting. The product weighs about 12g, with input and output isolated. It is applied in DC power supply systems to realize the isolated voltage conversion function. The module has the following characteristics.

Product Features

- 1. Enable control function
- 2. Fixed switching frequency
- 3. Input undervoltage protection
- 4. Withstands 50V surge voltage
- 5. Output short-circuit protection
- 6. Output over-current protection
- 7. Package: 1" \times 0.5"













1. Selection Guide

Product Model	Output Power (W)	Nominal Output voltage/Current	Efficiency (@28VDC, %/Typ.)	Max. Capacitive Load (μF)
D28C05M20BP	20	5V/4.00A	84	1000
D28C12M20BP	20	12V/1.67A	87	470
D28C15M20BP	20	15V/0.66A	84	470
D28C24M20BP	20	24V/0.83A	89	220
D28C28M20BP	20	28V/0.71A	84	220
D28C12DM20BP	20	±12V/1.67A	82	220
D28C15DM20BP	20	±15V/0.66A	87	220

2. Environmental Specifications

		_			
Item	Min.	Тур.	Max.	Unit	Remarks
Operating temperature	-55	25	105	°C	Baseplate temperature
Storage temperature	-55	25	125	°C	
Relative humidity	-	_	95	%	non-condensing
Pin Soldering Resistance Temperature	_	_	300	°C	Soldering time shall not exceed 10 seconds

3. Electrical Specifications

Input Specifi	cations	Condition	Minimum	Typical	Maximum	Unit
Input voltage range		Iout=0~100%Io	16	28	40	
Surge Voltage		50ms	-0.7	-	50	
Input	Starting voltage	Iout=0~100%Io	-	-	15.8	
undervoltage protection	Turn-off voltage	10ut=0~100%10	11	1	-	V
Enable control	Starting voltage	Ctrl to high or floating	3.5	-	12.0	
voltage ^a (positive logic)	Turn-off voltage	Ctrl to low or ground	0	-	0.7	



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No-load power consumption	Vin=16V~40V,no-load	-	-	1	W
Temperature coefficient	Full load	-	-	0.02	%°C

	Minimum	Typical	Maximum	Unit	
Vin=16V∼40V full load		-	ı	±2	%Vo
Vin=16V~40V	Vin=16V~40V			Guide	Α
Vin=16V~40V full load		-	-	±1	%
Vin=28V No-load→full load		-	ı	±1	%
	05V	-	-	100	
	12V	-	-	120	
	15V	-	-	120	
external 1µF ceramic capacitor and	24V	-	-	150	mV
10μF ceramic capacitor	28V	-	-	150	
	±12V	-	-	120	
	±15V	-	-	150	
Vin=16V~40V Iout=0~100%Io	Vin=16V~40V Iout=0~100%Io			170	%Vo
Hiccup mode		110	-	190	%Io
Housing operating temperature		-	110	-	%°C
Vin=28V full load		Refer to Selection Guide			%
Guaranteed when output is down Iout≤100%lo Guaranteed when output is up Po≤20W		90	-	110	%Vo
Iout: 25%load → 50%load → 25%load	50%load→	-	-	±5	%Vo
75%load→50%load di/dt=0.1A/us	50 7010dd 7	-	-	500	μ S
Vin=0V→28V full load Time from power-on to the output vo	-	10	20	ms	
Output rise time Vout rises from 10% to 90% full load		-	10	20	ms
Vin=16V~40V full load		-	-	3	%
Purely resistive load test,low ESR cap	pacitor,full load	Refer to Selection Guide			μF
Hiccup mode		Automatic recovery after fault removal			noval
	Vin=16V~40V Vin=16V~40V full load Vin=28V No-load→full load Full load,BW=20MHz Output external 1µF ceramic capacitor and 10µF ceramic capacitor Vin=16V~40V Iout=0~100%Io Hiccup mode Housing operating temperature Vin=28V full load Guaranteed when output is down Iout≤100%lo Guaranteed when output is up Po≤20W Iout:25%load→50%load→25%load 75%load→50%load di/dt=0.1A/us Vin=0V→28V full load Time from power-on to the output version 10% Vout rises from 10% to 90% full load Vin=16V~40V full load Purely resistive load test,low ESR capacitors Vin=16V~40V full load Purely resistive load test,low ESR capacitors Vin=16V~40V full load	Vin=16V~40V Vin=16V~40V full load Vin=28V No-load→full load Full load,BW=20MHz Output external 1µF ceramic capacitor and 10µF ceramic capacitor Vin=16V~40V Iout=0~100%Io Hiccup mode Housing operating temperature Vin=28V full load Guaranteed when output is down Iout≤100%lo Guaranteed when output is up Po≤20W Iout:25%load→50%load→25%load 50%load→75%load→50%load di/dt=0.1A/us Vin=0V→28V full load Time from power-on to the output voltage rising to 10% Vout rises from 10% to 90% full load Vin=16V~40V full load Purely resistive load test,low ESR capacitor,full load	Vin=16V~40V Refe Vin=16V~40V full load - Vin=28V No-load→full load - Full load,BW=20MHz Output external 1µF ceramic capacitor and 10µF ceramic capacitor 24V 28V - ±12V - ±12V - ±15V - Vin=16V~40V Iout=0~100%Io 110 Hiccup mode 110 Housing operating temperature - Vin=28V full load Refe Guaranteed when output is down Iout≤100%lo 90 Guaranteed when output is up Po≤20W 90 Iout:25%load→50%load→25%load 50%load→75%load→50%load→10/dt=0.1A/us - Vin=0V→28V full load - Time from power-on to the output voltage rising to 10% - Vout rises from 10% to 90% full load - Vin=16V~40V full load - Purely resistive load test,low ESR capacitor,full load Refe	Vin=16V~40V Refer to Selection (Vin=16V~40V full load) Refer to Sele	Vin=16V~40V Refer to Selection Guide

- a) When the Ctrl pin is connected to high level (3.5V \sim 12V) or left floating, the product operates normally. When it is connected to low level (0V \sim 0.7V), the product has no output.
- b) The overvoltage protection mode is constant voltage. After the overvoltage protection is released, the output voltage test result meets the electrical characteristic requirements.
- c) The parameters are guaranteed by the design and are only tested during identification and design or process changes.
- d) Recovery time refers to the time from the beginning of the transition until the output voltage returns to the corresponding stable value within \pm 2%.
- e) The start-up delay time can be calculated either from the power supply's transition or from the time when the ctrl terminal is connected to a low level, until the output voltage rises to 10% Vout.
- f) Capacitive loads do not affect the DC parameters.
- Note: The above specification parameter test circuit refers to the typical application 4.2 and 4.3.

General Specifications		Condition	Minimum	Typical	Maximum	Unit
Insulation resi	stance [®]	Add 500VDC between input and output, between input and shell, between output and shell for 10s	100	-	-	МΩ
	Input-Output		1500	-	-	
Isolation voltage ^{gh}	Input-Housing	t=1min set the leakage current to 1mA	1500	-	-	VDC
J	Output-Housing		500	-	-	

g) The input leads are pins 1, 2 and 3, and the output leads are pins 4, 5, 6. During the test, the input leads need to be shorted together, and the output leads need to be shorted together;

h) Judgment criteria: the module shall be free of breakdown and arcing.



Physical characteristics	
Dimension	25.40*12.70*10.80mm
Weight	12g±3g (Type)
Cooling Method	Conduction Heat Dissipation

4. Typical Applications

4.1 Enable Control

The function of the positive and negative enable logic is as follows:

For positive logic enable, the module works normally when the control pin is connected to high level or floating, and is turned off when grounded or low level. For negative logic enable, the module works normally when the control pin is grounded or at low level, and is turned off when connected to high level or floating;

The enable pin of this model is positive logic. When the enable pin is left floating (or connected to high level), the product has output. When not in use, the enable pin can be left floating; when using the enable pin, the product has no output when the enable pin is connected to the input ground (or connected to low level) by means of a switch, etc.

Switching mode	Triode control mode	Optocoupler isolation control mode	Logic gate control mode
SI Vin-	Ctrl	Ctrl Vin-	VCC Ctrl O Vin-

4.2 Application Diagram

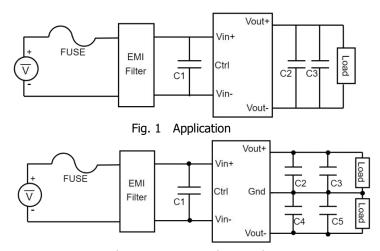


Fig. 2 Dual-output wiring application diagram

Fig. 1,2 shows the typical application connection method of the module. The input terminals of the module power supply will have significant differences due to the length of the input source leads. In order to prevent input oscillation caused by excessively long input lines, it is recommended to add input capacitors near the input pins of the module. Similarly, an output capacitor should be added at the output end of the module:

Recommend parameters								
C1	Input capacitance:	47μF electrolytic	capacitor					
	Output capacitance	: The values in t	he table below a	are electrolytic ca	apacitors.			
C2、C4	C2 C4 Output voltage (V) 5 12 15 24 28 ±12 ±15							



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	Value selection for C2 (µF)	100	68	47	47	47	68	47
C3、C5	C5 Output capacitance: 1μF ceramic capacitor							
The above parameters can be adjusted according to the actual system application requirements, select the appropriate parameter values.								

4.3 Output Ripple Voltage Test Diagram

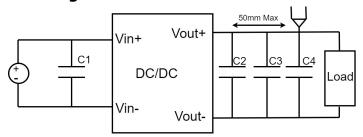


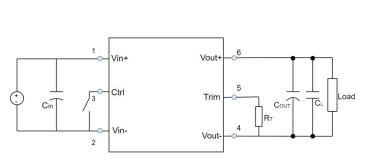
Fig.3 Schematic diagram of output ripple voltage

Ripple measurement is generally measured under the condition of rated input and output, the oscilloscope bandwidth is set to 20MHz, and the oscilloscope probe with the ground clamp removed is used to measure at a distance of about 3~5cm from the output end.

Note: The oscilloscope uses a bandwidth of 20MHz.

Recomn	nend parameters									
C1	Requires mounting of	Requires mounting close to the input pins of the module, recommend 47 µF ceramic capacitor or solid-state capacitor								
	pins of the module t	Ceramic capacitors with the capacitance values listed in the table below, which are required to be installed close to the output pins of the module to better reduce the output ripple voltage and improve the output characteristics of the product in high and low temperature environments.								
C2	Output voltage (V)	5	12	15	24	28	±12	±15		
	Value selection for C2 (µF)	100 68 47 47 47 68 47								
С3	1µF ceramic capac	1μF ceramic capacitor								
C4	10μF tantalum cap	10μF tantalum capacitor or ceramic capacitor								

4.4 Trim Function Application Note



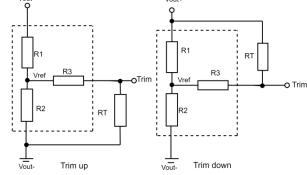


Fig.4: Functional application diagram of Trim

Fig.5 Trim circuit (dashed box is inside the product)

Trim resistance calculation formula:

up:
$$R_T = \frac{aR2}{R2-a} - R3$$

 $R_T = \frac{aR1}{R1-a} - R_3$

$$a = \frac{\frac{Vref}{Vo'-Vref}}{a = \frac{Vo'-Vref}{Vref}} \cdot R1$$

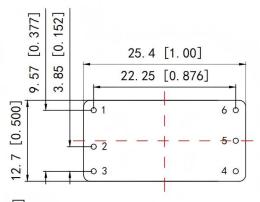
RT is Trim resistance

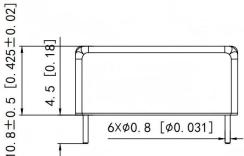
a is a custom parameter and has no actual meaning.



Model	R1(kΩ)	R2(kΩ)	R3(kΩ)	Vref(V)
D28C05M20BP	7.48	2.49	4.3	1.25
D28C12M20BP	9.49	2.49	4.3	2.5
D28C15M20BP	12.49	2.49	4.3	2.5
D28C24M20BP	21.5	2.49	4.3	2.5
D28C28M20BP	21.5	2.49	4.3	2.5

5. Dimension and Terminal Definition





NOTES:

- 1) First angle projection \bigcirc
- 2) Five-sided metal aluminum, anodized matte black
- 3) All dimension in mm[inches]
- 4) Pins diameter tolerance: ±0.1[0.004]
- 5) No specification for tolerance: X, X±0.5[X, XX±0.02], X, XX±0.25[X, XXX±0.01]

		\ 		
V	3			4 0
	2	Top \	/iew	501
		Top v	yout)	301
	1			60

Recommended PCB hole size NOTE: Grid size is 2.54*2.54[0.1*0.1]

	Single-	Dual-channel			
No.	channel Symbol	Function	Symbol	Function	
1	Vin+	Input positive end	Vin+	Input positive end	
2	Vin-	Input negative terminal	Vin-	Input negative terminal	
3	Ctrl	Enable control end	Ctrl	Enable control end	
4	Vout-	Negative output terminal	Vout-	Negative output terminal	
5	Trim	Output voltage adjustment terminal	Gnd	output common ground	
6	Vout+	Output positive terminal	Vout+	Output positive terminal	

Fig. 6 Terminal Arrangement (Top View, Pin Up) and Appearance Dimension

6. Precautions

- 6.1. Do not reverse the polarity of the power supply. Pay attention to the input voltage range, which is $16V \sim 40V$;
- 6.2. Please use wide PCB leads or thick wires between the power module and the load, and keep the line voltage drop below 2% Vo to ensure that the output voltage of the power module remains within the specified range:
- 6.3. The measurement of voltage must be conducted at the root of the module terminals, eliminating the measurement errors caused by the test tooling fixtures.
- 6.4. The impedance of the lead may cause output voltage oscillation or large ripple. Please make sufficient evaluation before use;
- 6.5. Prevent product collision;
- 6.6. Pay attention to the "1" pin (or ESD) identification, according to the correct installation direction plate welding;
- 6.7. Heat sink or other heat dissipation measures should be installed to ensure that the shell temperature is lower than the maximum operating temperature specified by the product. The operating temperature range of the product is:-55 $^{\circ}$ C \leq TC \leq 105 $^{\circ}$ C;
- 6.8. Lead welding temperature is less than 300 °C, welding time should not be exceed 10 seconds;
- 6.9 The heat dissipation surface of the product: either fasten it with screws, or apply thermal paste on the contact surface for heat dissipation; in addition, glue should be applied around the perimeter for fixation.



DC-DC Converters

Note:

- 1. Our products shall be classified and stored according to ISO14001 and relevant environmental laws and regulations after being scrapped, and shall be handled by qualified units;
- 2. Except for special instructions, all indicators in this manual are measured when Ta = 25 °C, humidity <75%, nominal input voltage 28V and output rated load;
- 3. The test methods of all indicators in this manual are based on the company's enterprise standards;
- 4. Our company can provide customized products, specific needs can directly contact our technical personnel;
- 5. If the product involves multi-brand materials, please refer to the manufacturer's standards for differences such as different colors.

DONGGUAN AMCHARD-POWER TECHNOLOGY CO., LTD.